

## Silicon PNP Power Transistors

2SB541

**DESCRIPTION**

- With TO-3 package
- High power dissipation

**APPLICATIONS**

- For power switching and general purpose applications

**PINNING(see Fig.2)**

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

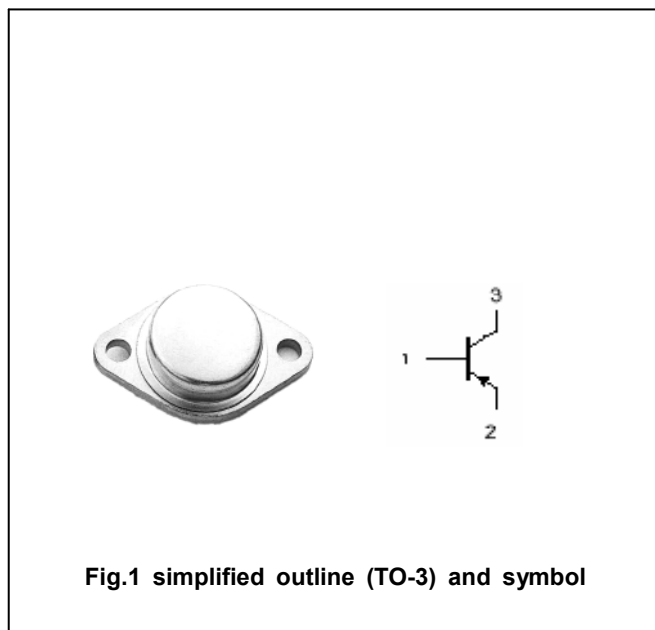


Fig.1 simplified outline (TO-3) and symbol

**Absolute maximum ratings(Ta=□)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	-110	V
$V_{CEO}$	Collector-emitter voltage	Open base	-110	V
$V_{EBO}$	Emitter-base voltage	Open collector	-6	V
$I_C$	Collector current		-8	A
$I_B$	Base current		-3	A
$P_C$	Collector power dissipation	$T_C=25□$	80	W
$T_j$	Junction temperature		150	□
$T_{stg}$	Storage temperature		-65~150	□

## Silicon PNP Power Transistors

## 2SB541

## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-50mA ; I <sub>B</sub> =0	-110			V
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage	I <sub>C</sub> =-1mA ; I <sub>E</sub> =0	-110			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =-1mA ; I <sub>C</sub> =0	-6			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-3A; I <sub>B</sub> =-0.3A			-1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-110V; I <sub>E</sub> =0			-0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-6V; I <sub>C</sub> =0			-0.1	mA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =-1A ; V <sub>CE</sub> =-5V	40		200	
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =-1A ; V <sub>CE</sub> =-5V		9		MHz

Silicon PNP Power Transistors

2SB541

PACKAGE OUTLINE



Fig.2 outline dimensions (unindicated tolerance:±0.1mm)