

Silicon NPN Power Transistors

2N6258

DESCRIPTION

- With TO-3 package
- Low collector saturation voltage

APPLICATIONS

- Designed for audio amplifier and switching circuits applications

PINNING

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

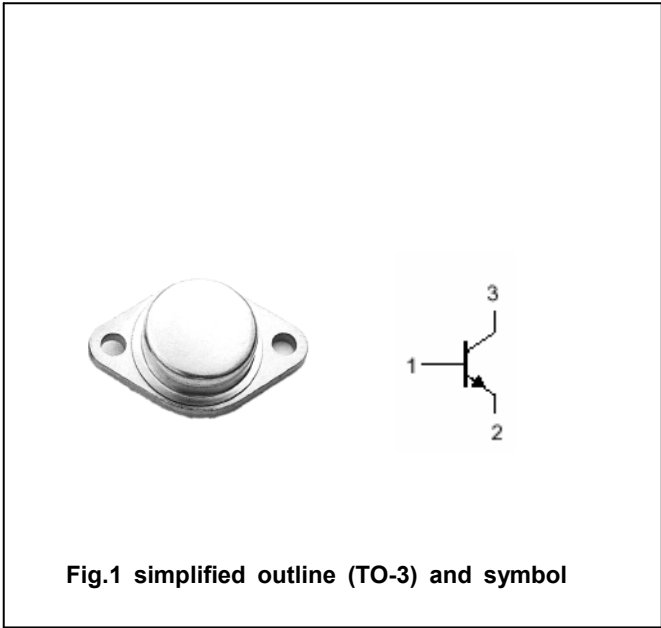


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	100	V
V _{CEO}	Collector-emitter voltage	Open base	80	V
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		30	A
I _B	Base current		7.5	A
P _D	Total Power Dissipation	T _C =25□	250	W
T _j	Junction temperature		150	□
T _{stg}	Storage temperature		-65~200	□

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-c}	Thermal resistance junction to case	0.875	□/W

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE0(SUS)}	Collector-emitter sustaining voltage	I _C =0.2A ; I _B =0	80			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =7.5A ; I _B =0.75A			1.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =7.5A ; I _B =0.75A			1.3	V
I _{CEO}	Collector cut-off current	V _{CE} =40V; I _B =0			1.0	mA
I _{CEV}	Collector cut-off current	V _{CE} =100V; V _{BE(off)} =1.5V T _C =150°C			0.1 5.0	mA
I _{CBO}	Collector cut-off current	V _{CB} =100V; I _E =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			0.1	mA
h _{FE}	DC current gain	I _C =15A ; V _{CE} =2V	20			
f _T	Transition frequency	I _C =1A; V _{CE} =10V	0.8			MHz

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PACKAGE OUTLINE



Fig.2 outline dimensions (unindicated tolerance:±0.10mm)